

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Low Thermal Resistance
- Super Trench
- Low ciss

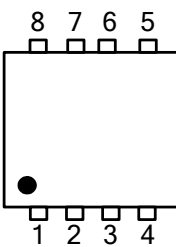
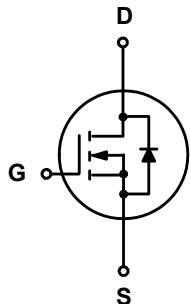
1.2 Applications

- Motor drivers
- DC - DC Converter

1.3 Quick reference

- $BV \leq 60\text{ V}$
- $R_{DS(ON)} \leq 12\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- $P_{tot} \leq 20.8\text{ W}$
- $R_{DS(ON)} \leq 20\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- $I_D \leq 50\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
4	Gate(G)	 <p>Top View PDFN3.3x3.3-8L</p>	
5,6,7,8	Drain(D)		
1,2,3	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	60	-	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 20	V
I _D *	Drain Current	T _C = 25 °C, V _{GS} = 10 V	-	50	A
I _{DM} *,**,***	Pulsed Source Current	T _C = 25 °C, V _{GS} = 10 V	-	112	A
P _{tot} *	Total Power Dissipation	T _C = 25 °C	-	20.8	W
T _{stg}	Storage Temperature		- 55	150	°C
T _J	Junction Temperature		-	150	°C
I _S	Diode Forward Current	T _C = 25 °C	-	50	A
R _{θJA} *	Thermal Resistance- Junction to Ambient		-	62.5	°C / W
R _{θJC} *	Thermal Resistance- Junction to Case		-	6	

Notes :

- * Surface Mounted on 1 in² pad area, t ≤ 10 sec
- ** Pulse width ≤ 10 μs, duty cycle ≤ 1 %
- *** limited by bonding wire

4. Marking Information

Product Name	Marking
N024N06QA	<div style="display: inline-block; background-color: black; color: white; padding: 2px;">024N06 YWWXXX</div> YWWXXXX : Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
N024N06QA	PDFN3.3*3.3			5000	

6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	60	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	1.0	2.0	3.0	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = 48 V, V _{GS} = 0 V	-	-	1	μA
		T _J = 85 °C	-	-	30	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(on)} ^a	Drain-Source On-State Resistance	V _{GS} = 10 V, I _D = 20A	-	11	12	mΩ
		V _{GS} = 4.5 V, I _D = 10A	-	18	20	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 20 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = 20 A, dI _{SD} /dt = 100 A/μs	-	53	-	ns
Q _{rr}	Reverse Recovery Charge		-	24	-	nC
Dynamic Characteristics^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 30 V Frequency = 1 MHz	-	789	-	pF
C _{oss}	Output Capacitance		-	376	-	
C _{rss}	Reverse Transfer Capacitance		-	32	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 30 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 1.5Ω, I _D = 20 A	-	6.5	-	ns
t _r	Turn-on Rise Time		-	29	-	
t _{d(off)}	Turn-off Delay Time		-	12.6	-	
t _f	Turn-off Fall Time		-	18.8	-	
Gate Charge Characteristics^b						
Q _g	Total Gate Charge	V _{GS} = 10 V, V _{DS} = 30 V, I _{DS} = 20 A	-	16	-	nC
Q _{gs}	Gate-Source Charge		-	4	-	
Q _{gd}	Gate-Drain Charge		-	3.5	-	

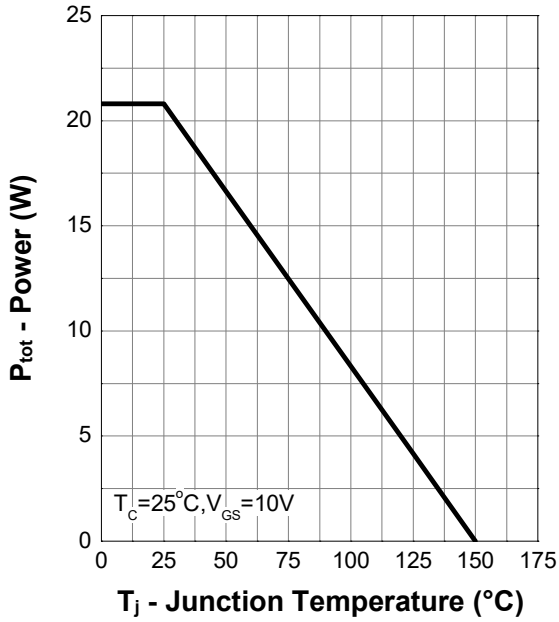
Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

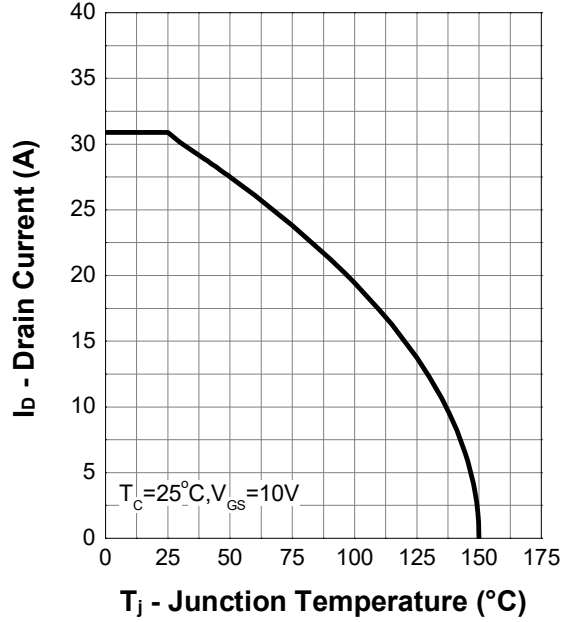
b : Guaranteed by design, not subject to production testing

7. Typical Characteristics (Cont.)

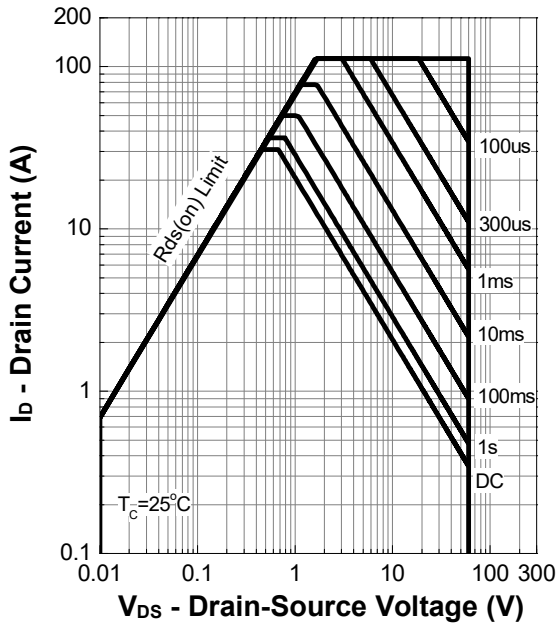
Power Capability



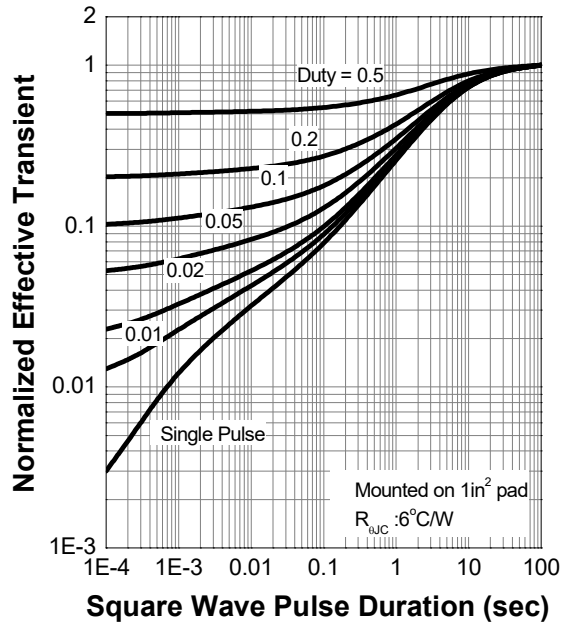
Current Capability



Safe Operation Area

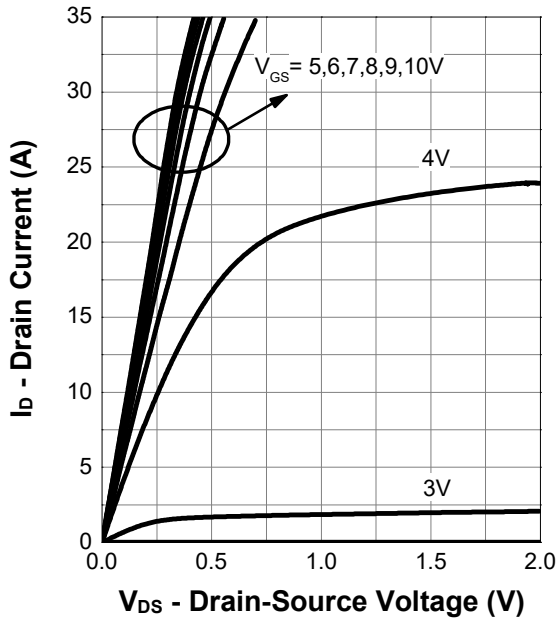


Transient Thermal Impedance

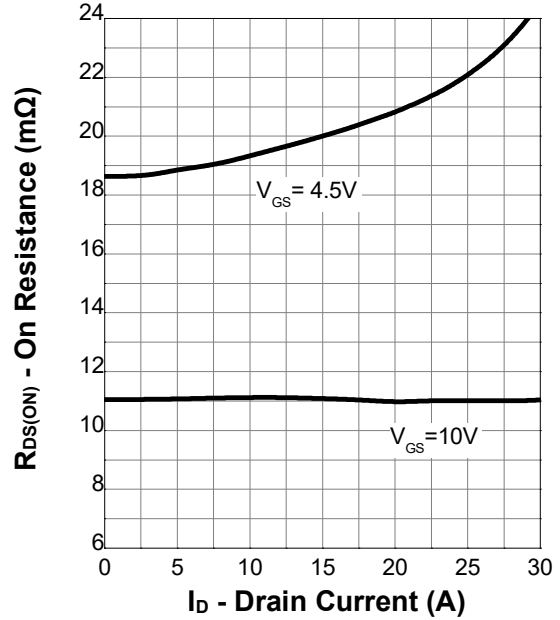


7. Typical Characteristics (Cont.)

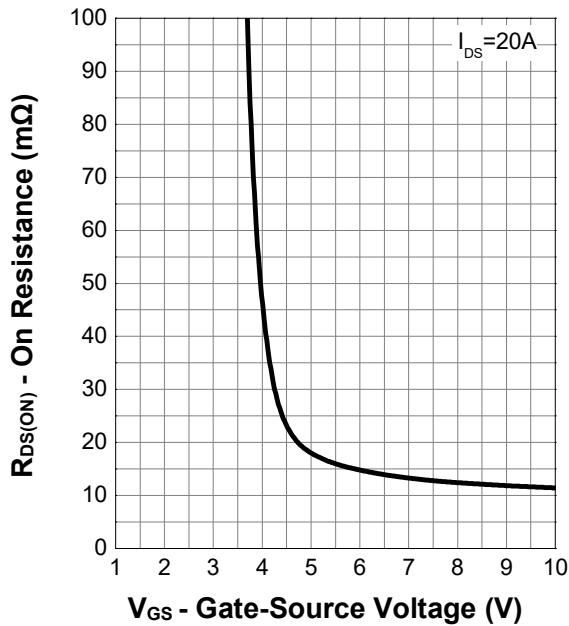
Output Characteristics



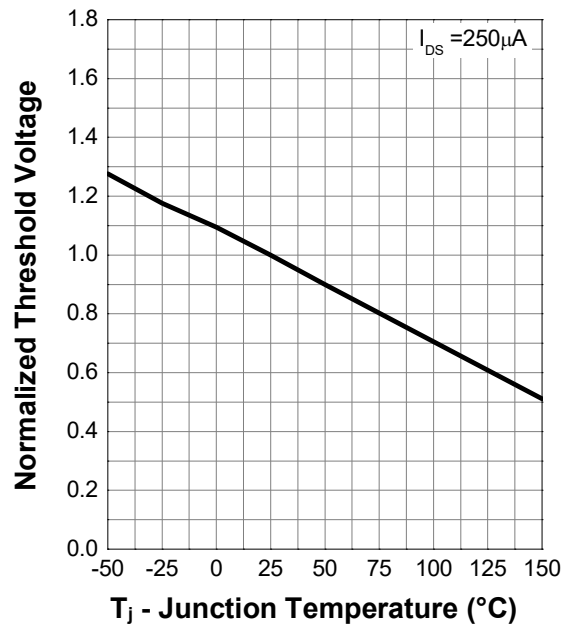
On Resistance



Transfer Characteristics

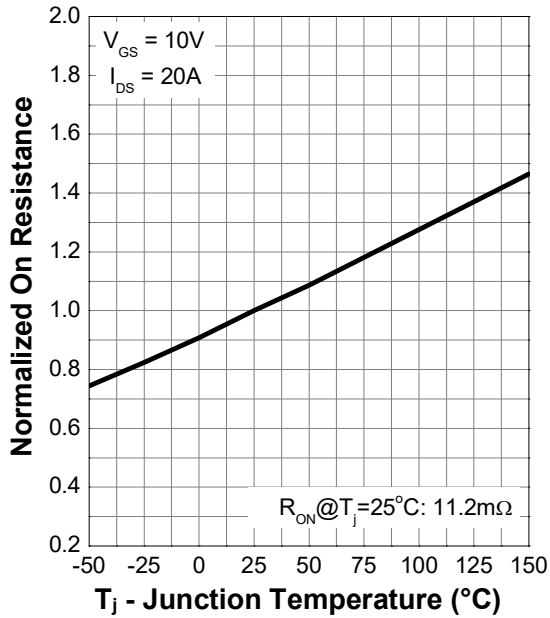


Normalized Threshold Voltage

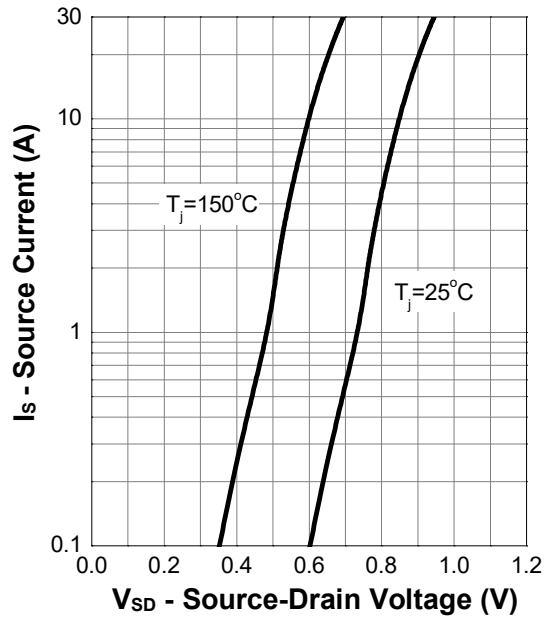


7. Typical Characteristics (Cont.)

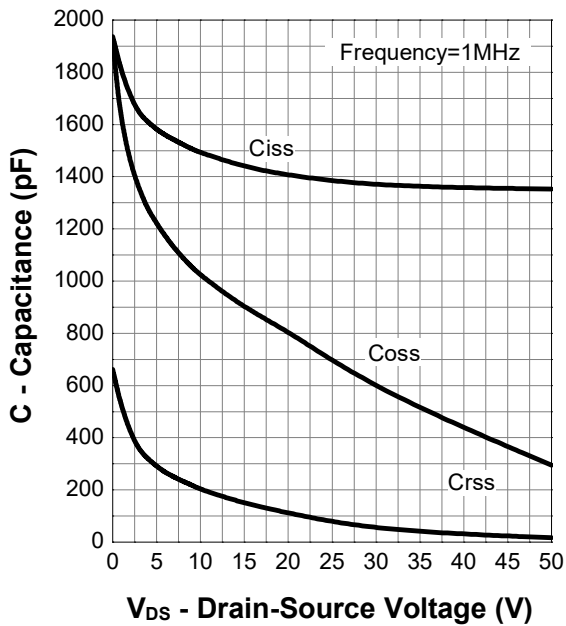
Normalized On Resistance



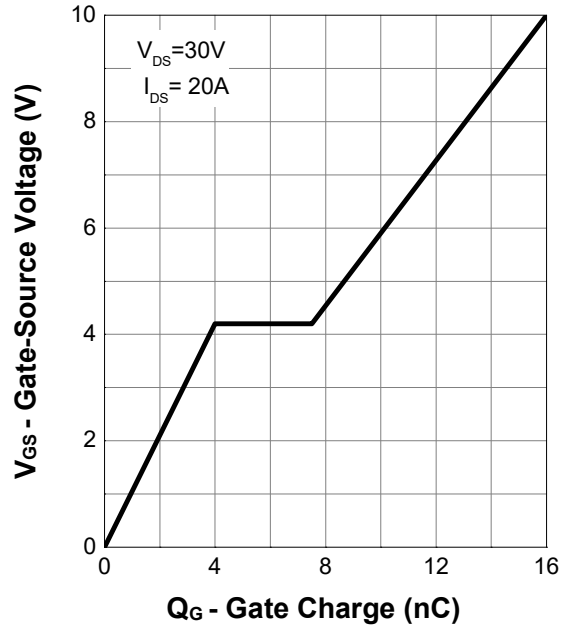
Diode Forward Current



Capacitance

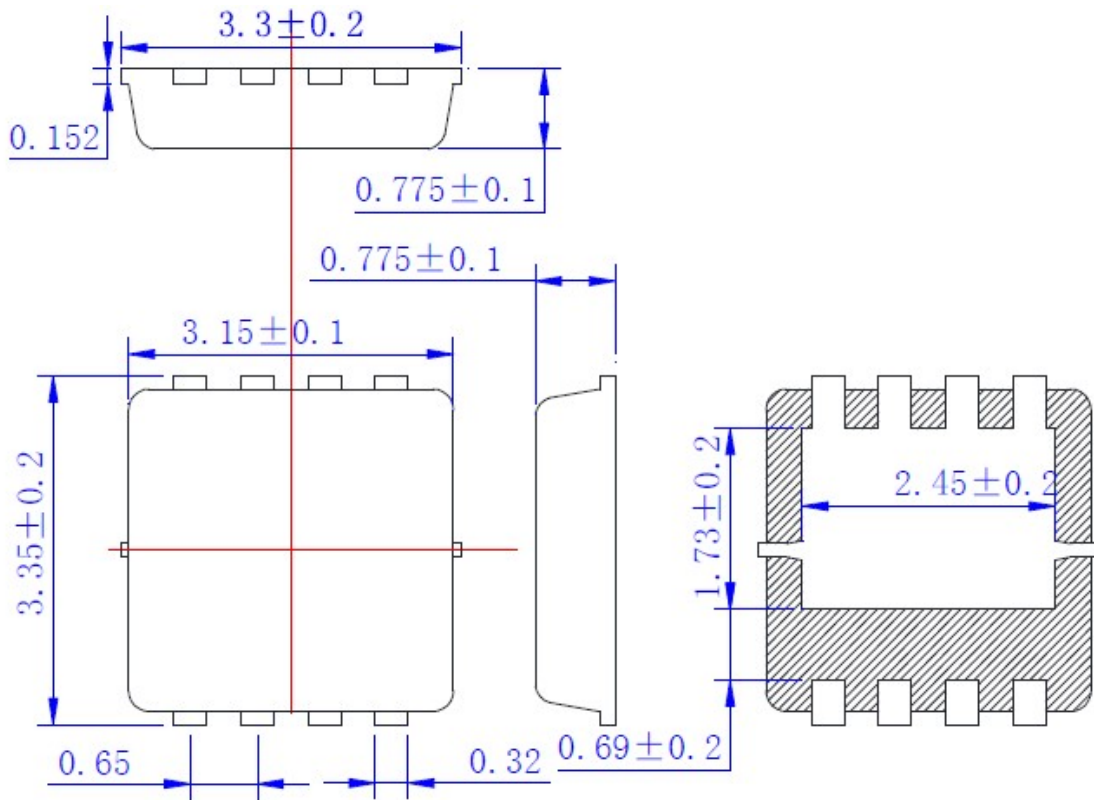


Gate Charge



8. Package Dimensions

PDFN3.3*3.3



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