

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Advanced trench cell design
- ☒ Low Thermal Resistance

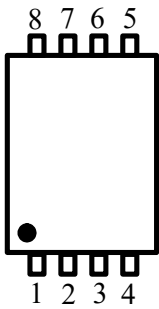
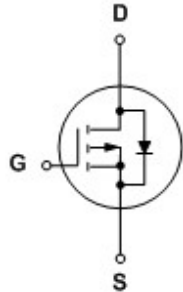
1.2 Applications

- ☒ Motor drivers
- ☒ DC - DC Converter

1.3 Quick reference

- ☒ $BV \geq -30\text{ V}$
- ☒ $P_{tot} \leq 78\text{ W}$
- ☒ $I_D \leq -150\text{ A}$
- ☒ $R_{DS(ON)} \leq 2.0\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- ☒ $R_{DS(ON)} \leq 4.2\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 Top View DFN5x6-8L	
4	Gate		
5,6,7,8	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	-	- 30	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 20	V
I _D *	Drain Current (DC)	T _C = 25 °C, V _{GS} = - 10 V	-	- 150	A
		T _C = 100 °C, V _{GS} = - 10 V	-	- 94	A
I _{DM} *,**	Drain Current (Pulsed)	T _C = 25 °C, V _{GS} = - 10 V	-	- 600	A
P _{tot} *	Total Power Dissipation	T _C = 25 °C	-	78	W
T _{stg}	Storage Temperature		- 55	150	°C
T _J	Junction Temperature		-	150	°C
I _S	Diode Forward Current	T _C = 25 °C	-	- 150	A
E _{AS} *	Single Pulsed Avalanche Energy	V _{DD} = - 30 V , L = 1 mH	-	760	mJ
R _{θJA} *	Thermal Resistance- Junction to Ambient		-	55	°C / W
R _{θJC} *	Thermal Resistance- Junction to Case		-	1.6	

Notes :

- * Surface Mounted on 1 in² pad area, t ≤ 10 sec
- ** Pulse width ≤ 300 μs, duty cycle ≤ 2 %
- *** limited by bonding wire

4. Marking Information

Product Name	Marking
N002P03G	002P03 YWWXXX YWWXXX: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
N002P03G	PDFN5*6			5000	

6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

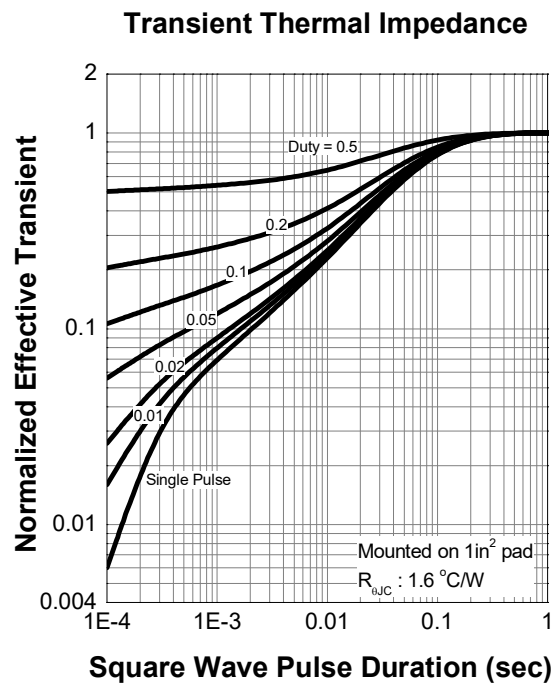
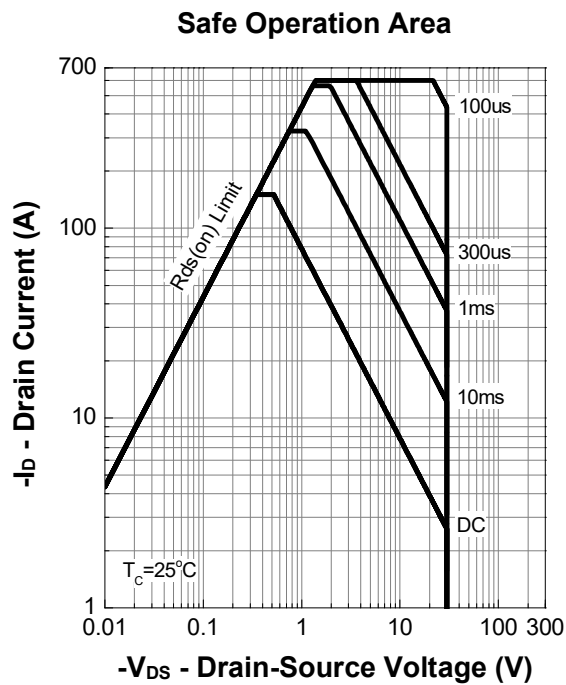
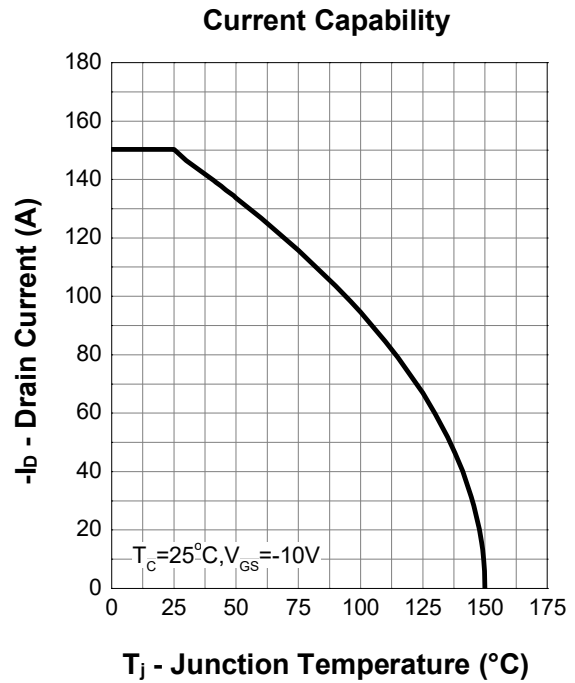
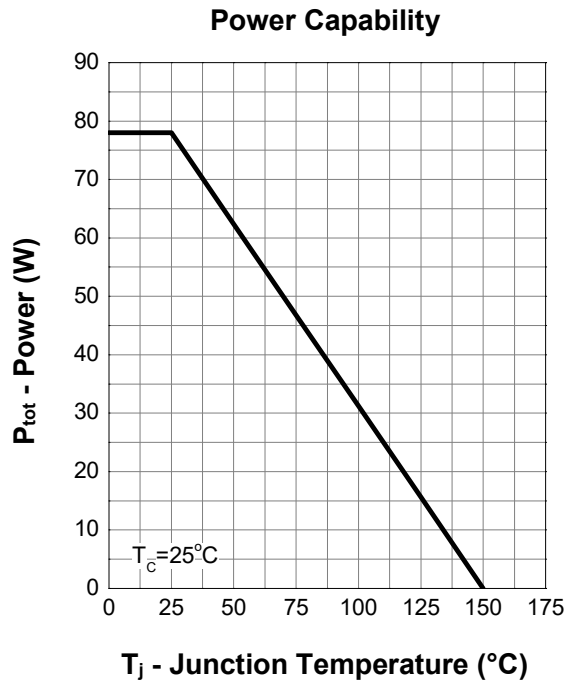
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = - 250 μA	- 30	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = - 250 μA	- 1	-	- 3	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = - 24 V, V _{GS} = 0 V	-	-	- 1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = - 10 V, I _D = - 50 A	-	1.8	2.0	m Ω
		V _{GS} = - 4.5 V, I _D = - 30 A	-	3.8	4.2	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = - 50 A, V _{GS} = 0 V	-	-	- 1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = - 30 A, dI _{SD} /dt = 100 A/μs	-	37	-	nS
Q _{rr}	Reverse Recovery Charge		-	30	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = - 15 V Frequency = 1 MHz	-	12977	-	pF
C _{oss}	Output Capacitance		-	1526	-	
C _{rss}	Reverse Transfer Capacitance		-	517	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = - 15 V, V _{GEN} = - 10 V, R _G = 3.9 Ω, R _L = 0.3 Ω, I _{DS} = - 50 A	-	21	-	nS
t _r	Turn-on Rise Time		-	195	-	
t _{d(off)}	Turn-off Delay Time		-	177	-	
t _f	Turn-off Fall Time		-	109	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} = - 15 V, V _{GS} = - 10 V, I _{DS} = - 50 A	-	212	-	nC
Q _{gs}	Gate-Source Charge		-	60	-	
Q _{gd}	Gate-Drain Charge		-	35	-	

Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

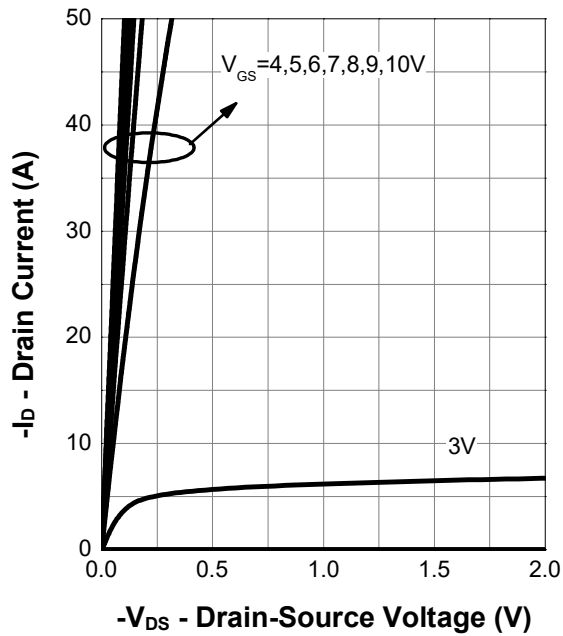
b : Guaranteed by design, not subject to production testing

7. Typical Characteristics (Cont.)

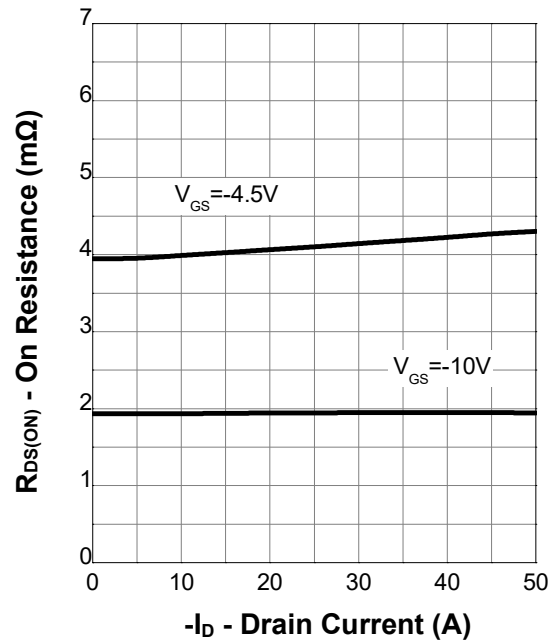


7. Typical Characteristics (Cont.)

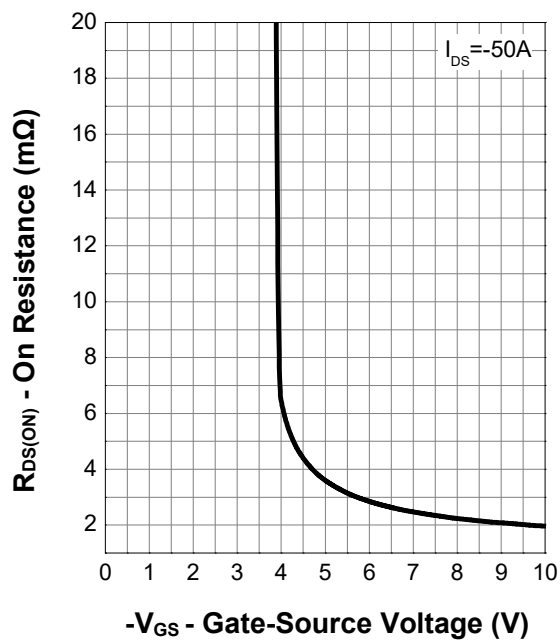
Output Characteristics



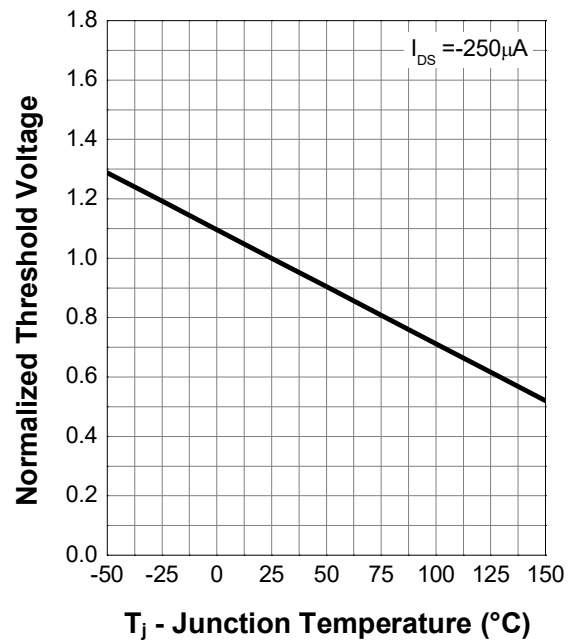
On Resistance



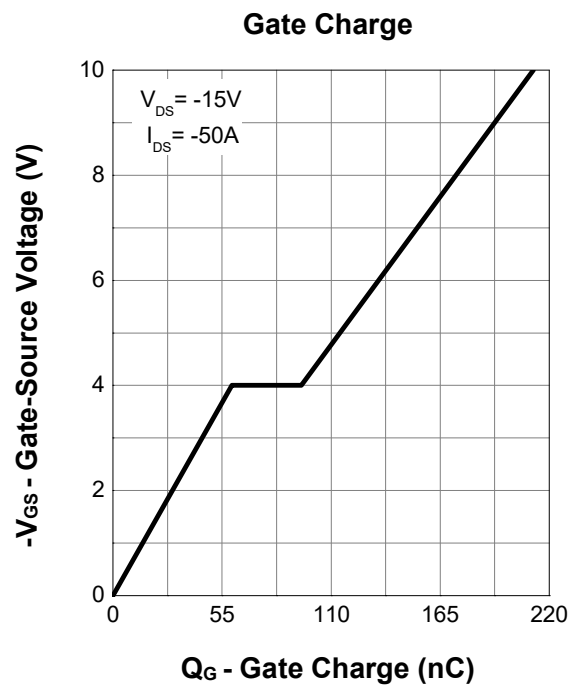
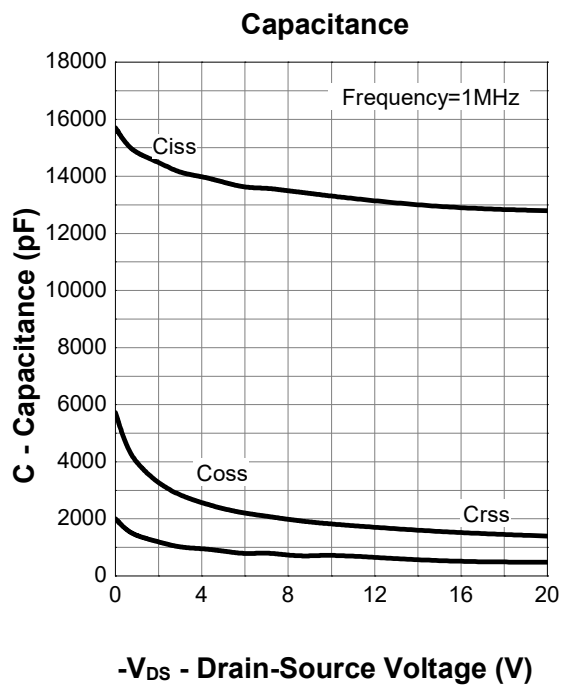
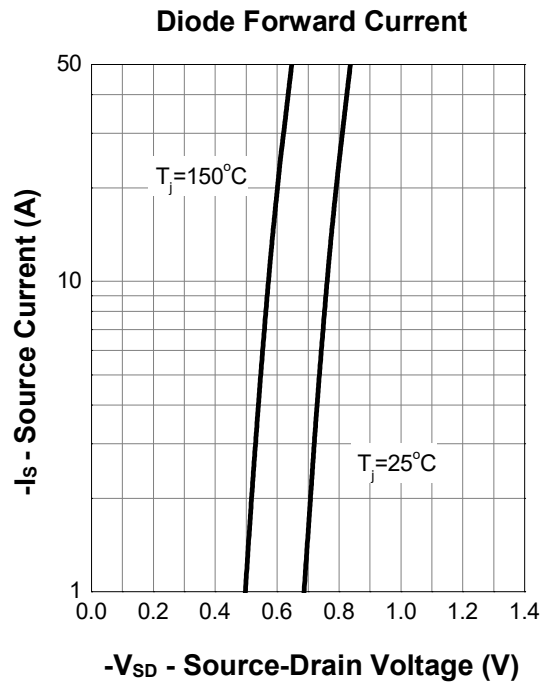
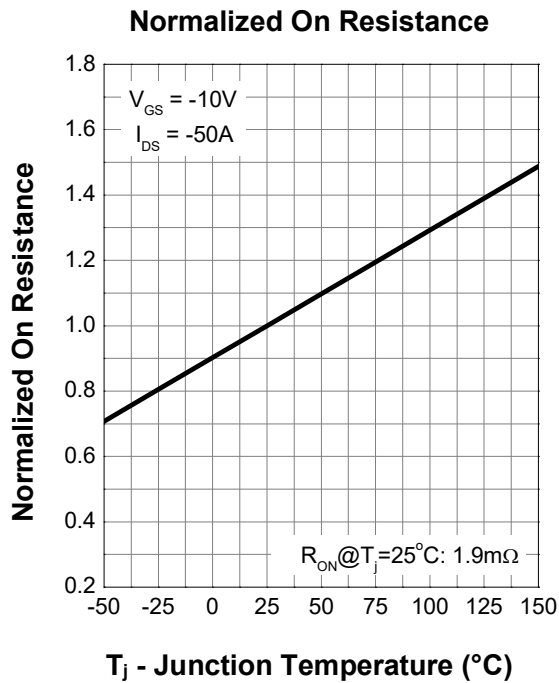
Transfer Characteristics



Normalized Threshold Voltage

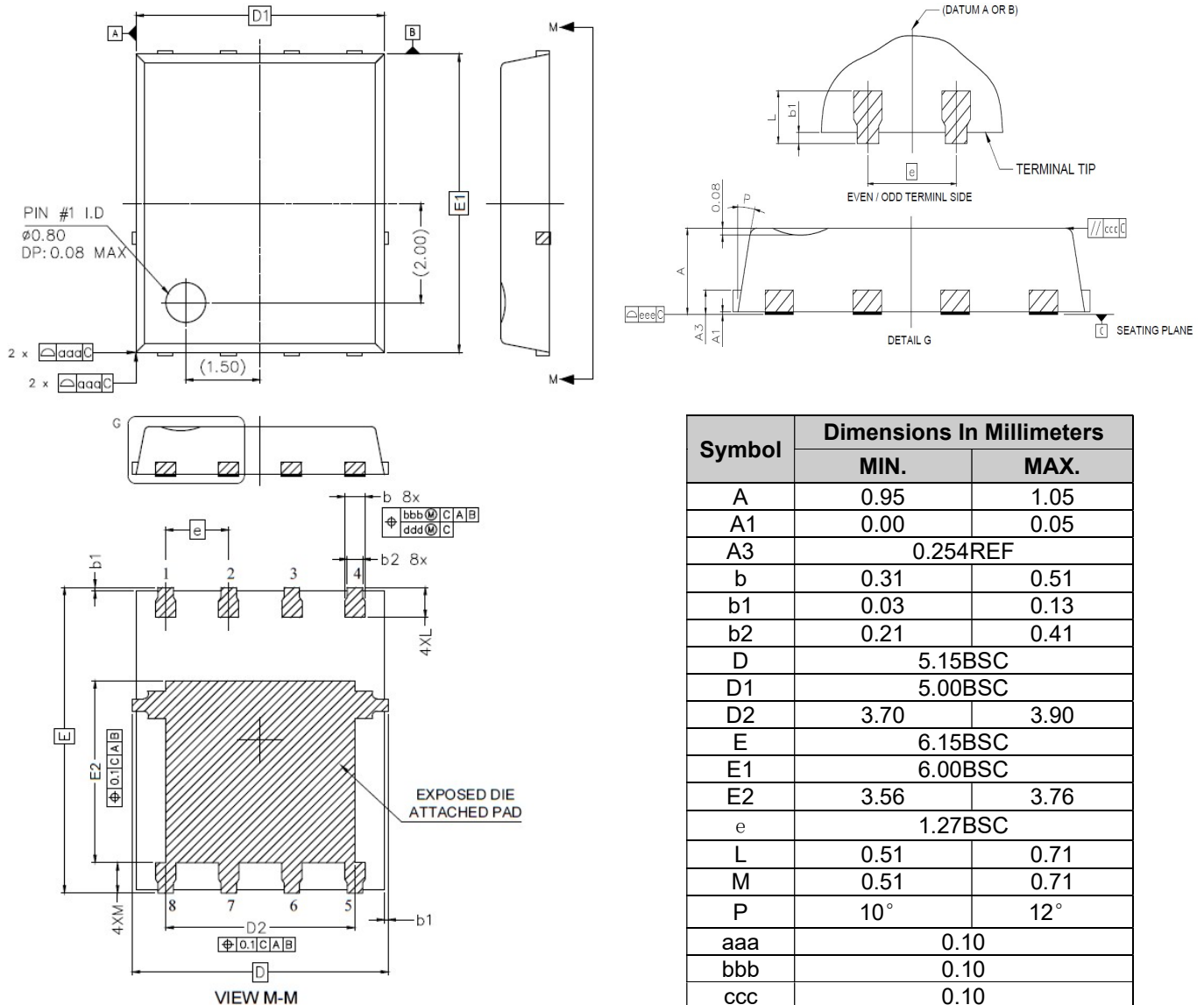


7. Typical Characteristics (Cont.)



8. Package Dimensions

PDFN5x6 - 8L Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	0.95	1.05
A1	0.00	0.05
A3	0.254REF	
b	0.31	0.51
b1	0.03	0.13
b2	0.21	0.41
D	5.15BSC	
D1	5.00BSC	
D2	3.70	3.90
E	6.15BSC	
E1	6.00BSC	
E2	3.56	3.76
e	1.27BSC	
L	0.51	0.71
M	0.51	0.71
P	10°	12°
aaa	0.10	
bbb	0.10	
ccc	0.10	
ddd	0.05	
eee	0.08	

NOTICE

JSHD reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. JSHD does not assume any liability arising out of the application or use of any product described herein.