

# P-Channel Enhancement Mode MOSFET

## 1. Product Information

### 1.1 Features

- ☒ Advanced trench cell design
- ☒ Low Thermal Resistance

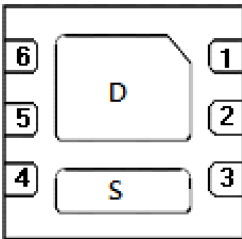
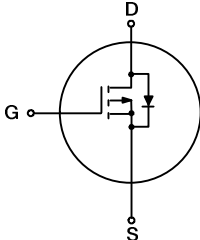
### 1.2 Applications

- ☒ Motor drivers
- ☒ DC - DC Converter

### 1.3 Quick reference

- ☒  $BV \geq -30\text{ V}$
- ☒  $P_{tot} \leq 2\text{ W}$
- ☒  $I_D \leq -10\text{ A}$
- ☒  $R_{DS(ON)} \leq 18\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- ☒  $R_{DS(ON)} \leq 28\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$

## 2. Pin Description

| Pin     | Description | Simplified Outline                                                                   | Symbol                                                                                |
|---------|-------------|--------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| 1,2,5,6 | Drain       |  |  |
| 3       | Gate        |                                                                                      |                                                                                       |
| 4       | Source      |                                                                                      |                                                                                       |
|         |             | Bottom View<br>DFN2X2-6L                                                             |                                                                                       |

### 3. Limiting Values

| Symbol            | Parameter                               | Conditions                                                | Min  | Max      | Unit                          |
|-------------------|-----------------------------------------|-----------------------------------------------------------|------|----------|-------------------------------|
| $V_{DS}$          | Drain-Source Voltage                    | $T_C = 25\text{ }^{\circ}\text{C}$                        | -30  | -        | V                             |
| $V_{GS}$          | Gate-Source Voltage                     | $T_C = 25\text{ }^{\circ}\text{C}$                        | -    | $\pm 20$ | V                             |
| $I_D^*$           | Drain Current                           | $T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = -10\text{ V}$ | -    | -10      | A                             |
| $I_{DM}^{*,**,*}$ | Pulsed Source Current                   | $T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = -10\text{ V}$ | -    | -96      | A                             |
| $P_{tot}^*$       | Total Power Dissipation                 | $T_C = 25\text{ }^{\circ}\text{C}$                        | -    | 2        | W                             |
| $T_{stg}$         | Storage Temperature                     |                                                           | - 55 | 150      | $^{\circ}\text{C}$            |
| $T_J$             | Junction Temperature                    |                                                           | -    | 150      | $^{\circ}\text{C}$            |
| $I_S$             | Diode Forward Current                   | $T_C = 25\text{ }^{\circ}\text{C}$                        | -    | -10      | A                             |
| $R_{\theta JC}^*$ | Thermal Resistance- Junction to Ambient |                                                           | -    | 6        | $^{\circ}\text{C} / \text{W}$ |

Notes :

\* Surface Mounted on 1 in<sup>2</sup> pad area,  $t \leq 10\text{ sec}$

\*\* Pulse width  $\leq 10\text{ }\mu\text{s}$ , duty cycle  $\leq 1\%$

\*\*\* Limited by bonding wire

### 4. Marking Information

| Product Name | Marking                                                   |
|--------------|-----------------------------------------------------------|
| N15P03N2     | <div>15P03<br/>YWWXXX</div> <div>YWW:<br/>Date Code</div> |

### 5. Ordering Code

| Product Name | Package | Reel Size | Tape width | Quantity | Note |
|--------------|---------|-----------|------------|----------|------|
| N15P03N2     | DFN2*2  |           |            |          |      |

Note: NHCX defines "Green" as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

## 6. Electrical Characteristics (T<sub>C</sub> = 25 °C Unless Otherwise Noted)

| Symbol                                   | Parameter                        | Conditions                                                                                                                       | Min  | Typ  | Max   | Unit |
|------------------------------------------|----------------------------------|----------------------------------------------------------------------------------------------------------------------------------|------|------|-------|------|
| Static Characteristics                   |                                  |                                                                                                                                  |      |      |       |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage   | V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA                                                                                  | -30  | -    | -     | V    |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage           | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>DS</sub> = -250 μA                                                                    | -1.0 | -    | -2.0  | V    |
| I <sub>DSS</sub>                         | Zero Gate Voltage Source Current | V <sub>DS</sub> = -24 V, V <sub>GS</sub> = 0 V                                                                                   | -    | -    | -1    | μA   |
|                                          |                                  | T <sub>J</sub> = 85 °C                                                                                                           | -    | -    | -30   | μA   |
| I <sub>GSS</sub>                         | Gate Leakage Current             | V <sub>GS</sub> = ± 20 V, V <sub>DS</sub> = 0 V                                                                                  | -    | -    | ± 100 | nA   |
| R <sub>DS(ON)</sub> <sup>a</sup>         | Drain-Source On-State Resistance | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -20 A                                                                                  | -    | 15   | 18    | mΩ   |
|                                          |                                  | V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10 A                                                                                 | -    | 24   | 28    | mΩ   |
| Diode Characteristics                    |                                  |                                                                                                                                  |      |      |       |      |
| V <sub>SD</sub> <sup>a</sup>             | Diode Forward Voltage            | I <sub>SD</sub> = -20 A, V <sub>GS</sub> = 0 V                                                                                   | -    | -    | -1.3  | V    |
| t <sub>rr</sub>                          | Reverse Recovery Time            | I <sub>SD</sub> = -20 A, dI <sub>SD</sub> /dt = 100 A/μs                                                                         | -    | 8.3  | -     | nS   |
| Q <sub>rr</sub>                          | Reverse Recovery Charge          |                                                                                                                                  | -    | 0.6  | -     | nC   |
| Dynamic Characteristics <sup>b</sup>     |                                  |                                                                                                                                  |      |      |       |      |
| C <sub>iss</sub>                         | Input Capacitance                | V <sub>GS</sub> = 0 V, V <sub>DS</sub> = -15 V<br>Frequency = 1 MHz                                                              | -    | 1811 | -     | pF   |
| C <sub>oss</sub>                         | Output Capacitance               |                                                                                                                                  | -    | 172  | -     |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance     |                                                                                                                                  | -    | 134  | -     |      |
| t <sub>d(on)</sub>                       | Turn-on Delay Time               | V <sub>DS</sub> = -15 V, V <sub>GEN</sub> = -10 V,<br>R <sub>G</sub> = 4.5 Ω, R <sub>L</sub> = 0.75 Ω,<br>I <sub>D</sub> = -20 A | -    | 18   | -     | nS   |
| t <sub>r</sub>                           | Turn-on Rise Time                |                                                                                                                                  | -    | 86   | -     |      |
| t <sub>d(off)</sub>                      | Turn-off Delay Time              |                                                                                                                                  | -    | 231  | -     |      |
| t <sub>f</sub>                           | Turn-off Fall Time               |                                                                                                                                  | -    | 127  | -     |      |
| Gate Charge Characteristics <sup>b</sup> |                                  |                                                                                                                                  |      |      |       |      |
| Q <sub>g</sub>                           | Total Gate Charge                | V <sub>GS</sub> = -10 V, V <sub>DS</sub> = -15 V,<br>I <sub>DS</sub> = -20 A                                                     | -    | 31   | -     | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge               |                                                                                                                                  | -    | 8.6  | -     |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge                |                                                                                                                                  | -    | 4.8  | -     |      |

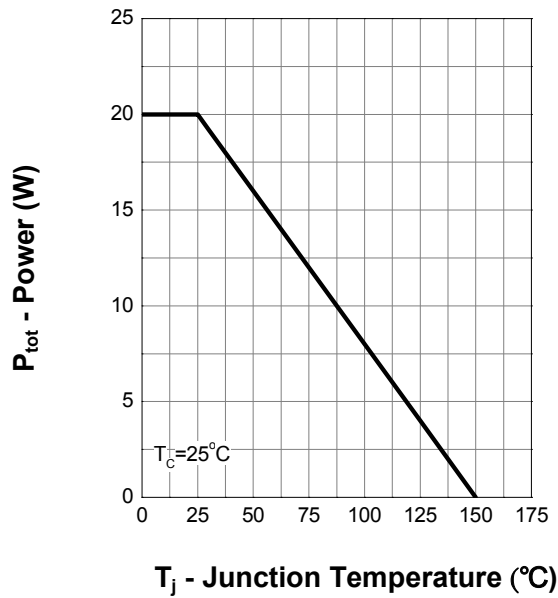
Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

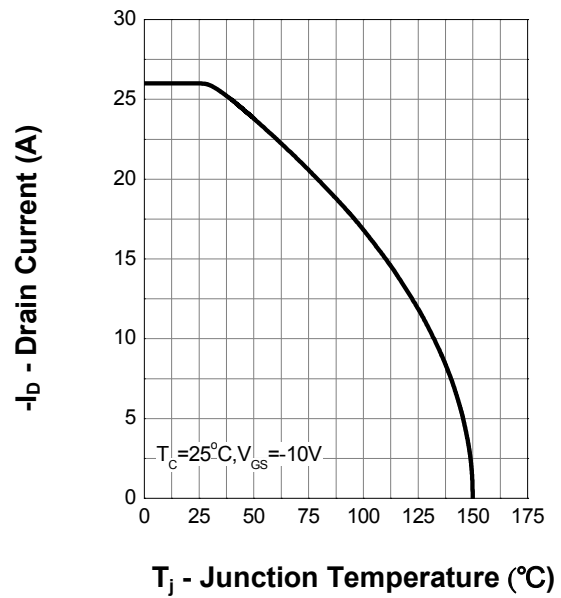
b : Guaranteed by design, not subject to production testing

## 7. Typical Characteristics

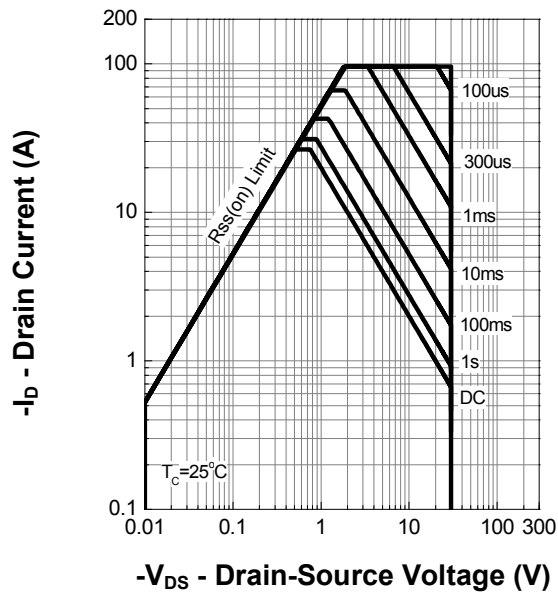
**Power Capability**



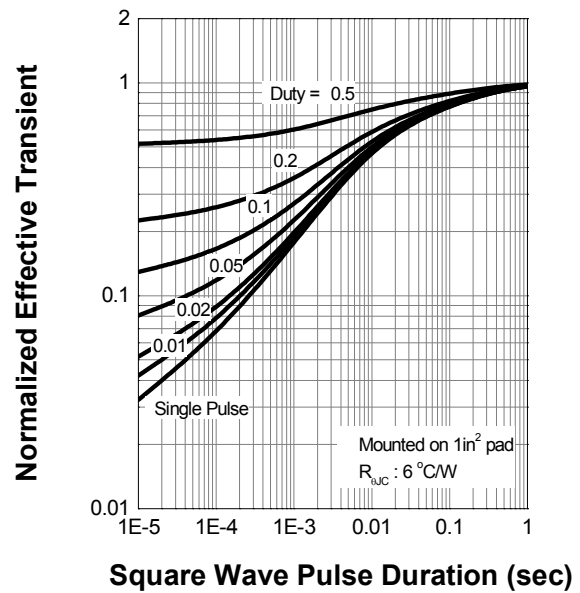
**Current Capability**



**Safe Operation Area**

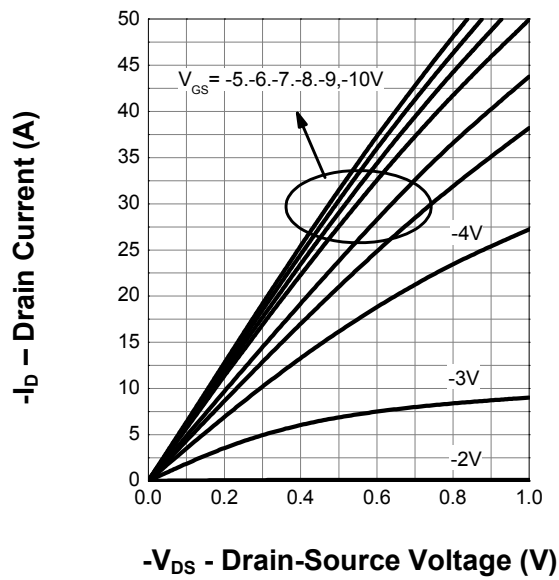


**Thermal Transient Impedance**

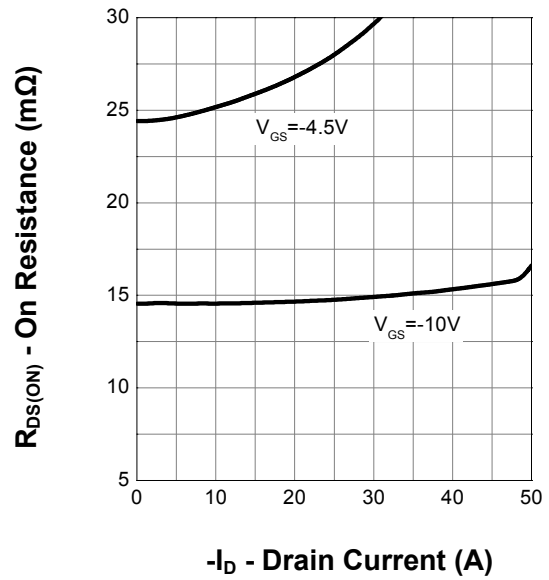


## 7. Typical Characteristics (cont.)

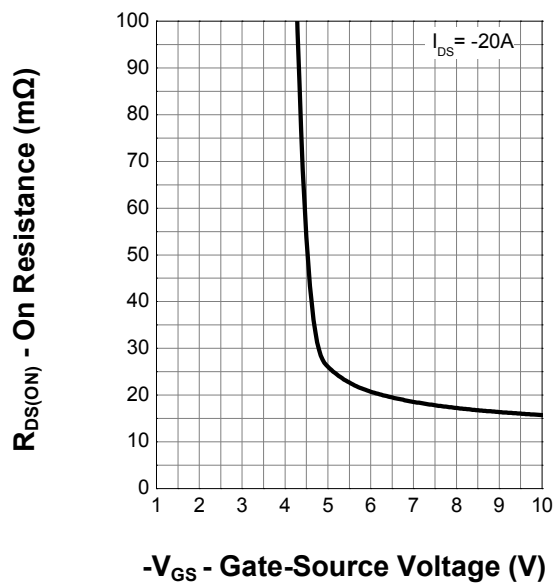
Output Characteristics



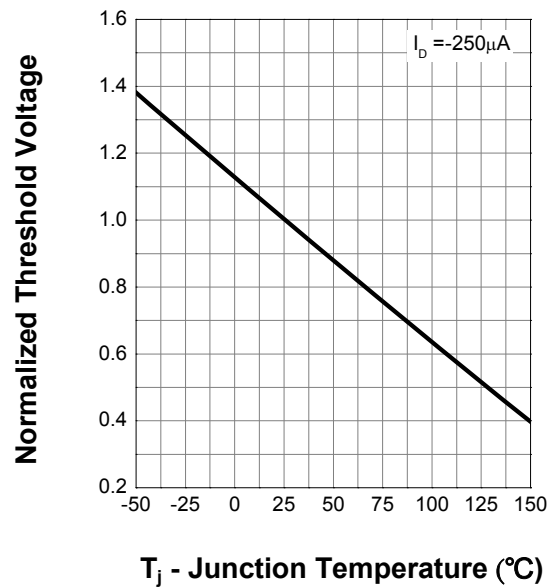
Drain-Source On Resistance



Transfer Characteristics

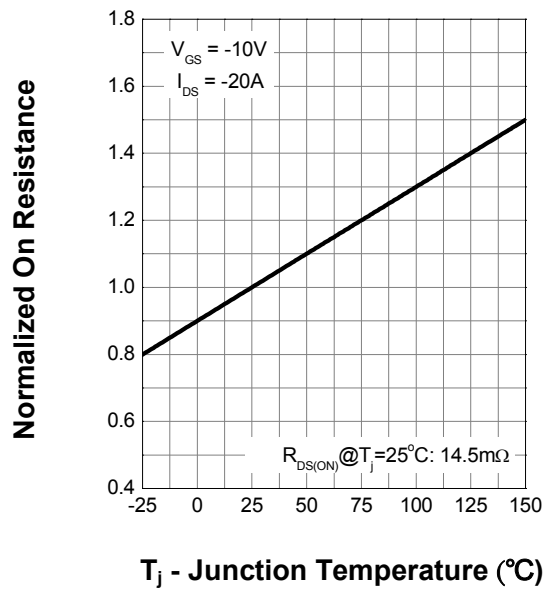


Gate Threshold Voltage

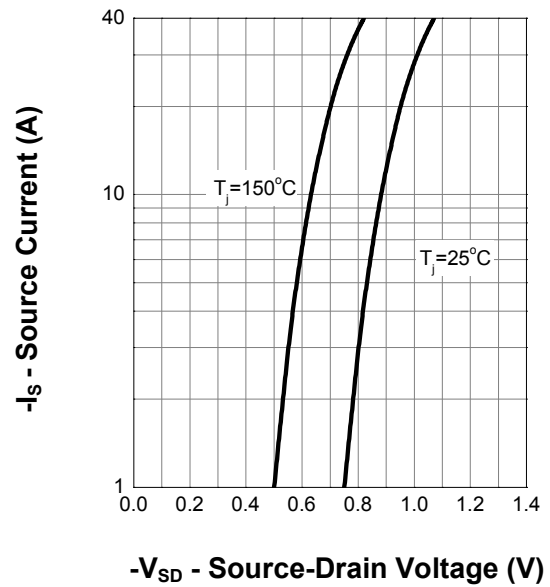


## 7. Typical Characteristics (cont.)

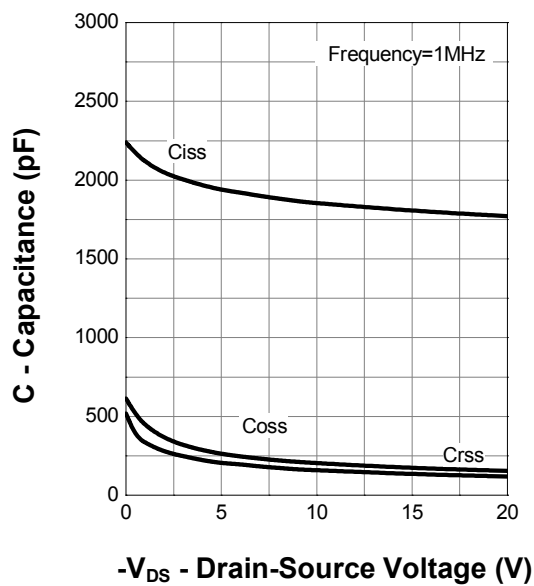
**Drain-Source On Resistance**



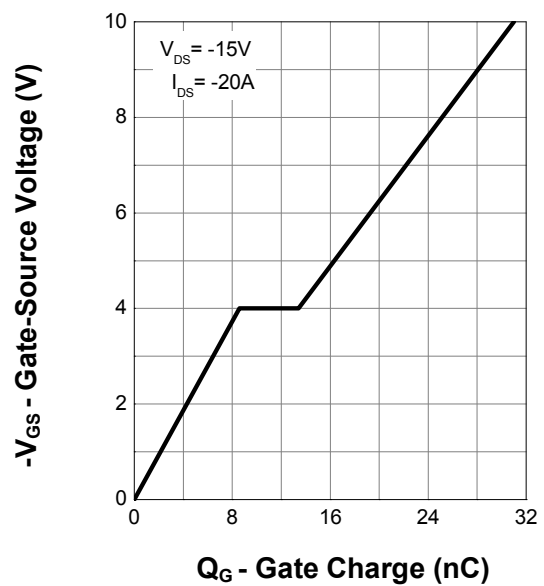
**Body Diode Characteristics**



**Capacitance**

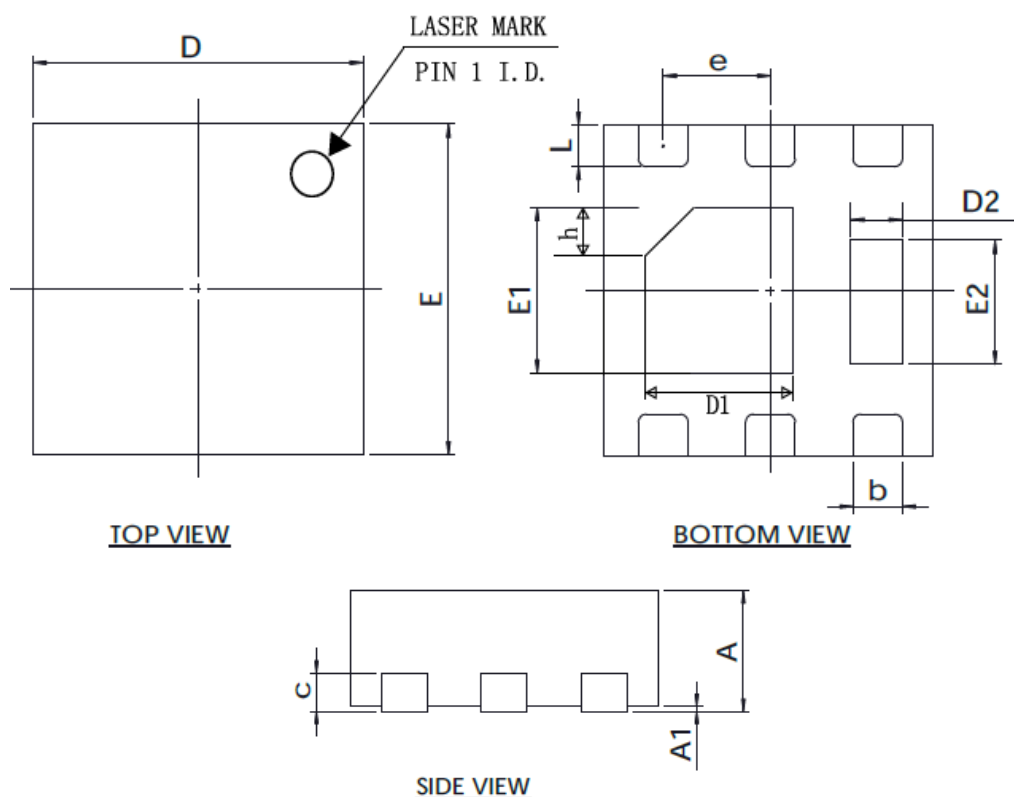


**Gate Charge**



## 8.Package Dimensions

### DFN2\*2-6L



| Symbol | MIN     | NOM  | MAX  |
|--------|---------|------|------|
| A      | 0.70    | 0.75 | 0.80 |
| A1     | NA      | 0.02 | 0.05 |
| b      | 0.20    | 0.27 | 0.34 |
| c      | 0.18    | 0.20 | 0.25 |
| D      | 1.95    | 2.00 | 2.07 |
| E      | 1.95    | 2.00 | 2.07 |
| D1     | 0.80    | 0.90 | 1.00 |
| E1     | 0.90    | 1.00 | 1.10 |
| D2     | 0.20    | 0.30 | 0.40 |
| E2     | 0.65    | 0.75 | 0.85 |
| L      | 0.20    | 0.25 | 0.35 |
| h      | 0.20    | 0.25 | 0.30 |
| e      | 0.65BSC |      |      |