

SMBG Plastic-Encapsulate Diodes

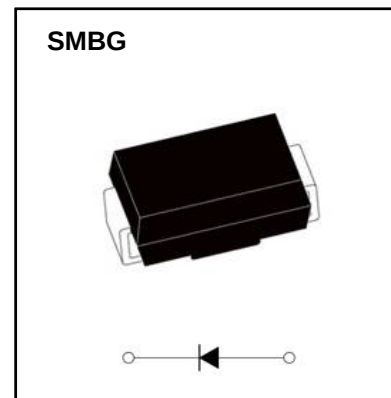
Super Fast Recovery Rectifier Diode

Features

- I_O 2A
- VRRM 50V-600V
- Low forward voltage drop
- High surge current capability
- Glass passivated chip junction

Mechical Data

- Case: JEDEC DO-214AA molded plastic
- Molding compound: UL flammability classification rating 94V-0
- Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102
- Polarity: Color band denotes cathode end



Limiting Values (Absolute Maximum Rating)

Item	Symbol	Unit	Test Conditions	ES2							
				A	B	C	D	E	G	H	J
Repetitive Peak Reverse Voltage	V_{RRM}	V		50	100	150	200	300	400	500	600
Maximum RMS Voltage	V_{RMS}	V		35	70	105	140	210	280	350	420
Maximum DC blocking Voltage	V_{DC}	V		50	100	150	200	300	400	500	600
Average Forward Current	$I_{F(AV)}$	A	60HZ Half-sine wave, Resistance load, $T_L=110^{\circ}C$	2.0							
Surge(Non-repetitive)Forward Current	I_{FSM}	A	60Hz Half-sine wave ,1 cycle , $T_a=25^{\circ}C$	50							
Junction Temperature	T_J	$^{\circ}C$		-55~+150							
Storage Temperature	T_{STG}	$^{\circ}C$		-55 ~ +150							

Electrical Characteristics ($T_a=25^{\circ}C$ Unless otherwise specified)

Item	Symbol	Unit	Test Condition		ES2							
					A	B	C	D	E	G	H	J
Peak Forward Voltage	V _F	V	I _F =2.0A		0.95				1.25		1.70	
Maximum reverse recovery time	t _{rr}	ns	I _F =0.5A,I _R =1.0A,I _{rr} =0.25A		35							
Peak Reverse Current	I _{RRM1}	μA	V _{RM} =V _{RRM}	T _a =25℃	5							
	T _a =125℃			100								
Thermal Resistance(Typical)	R _{θJ-A}	℃/W	Between junction and ambient		65							
	Between junction and terminal		25									
	R _{θJ-L}		Between junction and case		23							
Juction Capacitance (Typical)	C _J	pF	Measured at 1.0MHz and applied reverse voltage of 4.0 volts.		30				18		12	

Typical Characteristics

FIG.1: FORWARD CURRENT DERATING CURVE

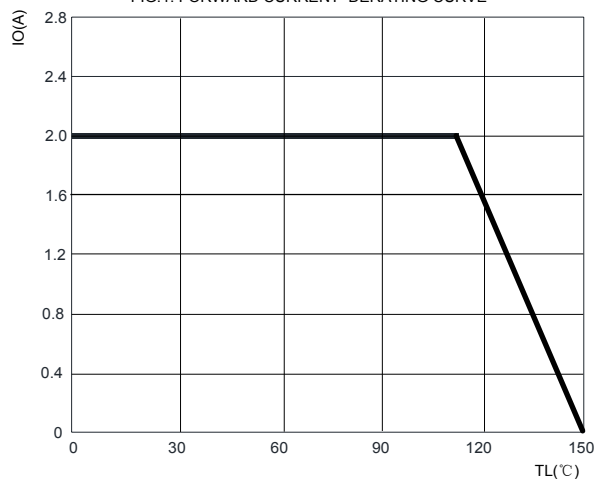


FIG.2: MAXIMUM NON-REPETITIVE FORWARD URGE CURRENT

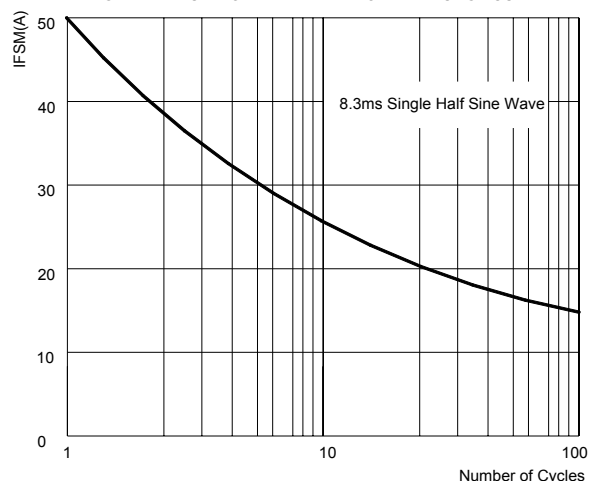


FIG.3: TYPICAL FORWARD CHARACTERISTICS

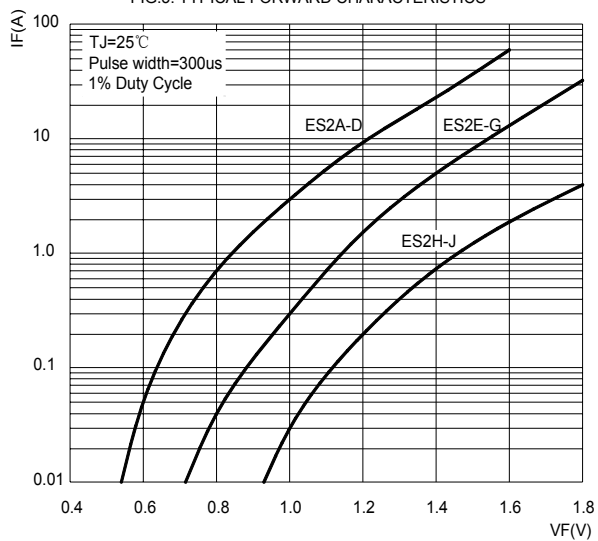


FIG.4: TYPICAL REVERSE CHARACTERISTICS

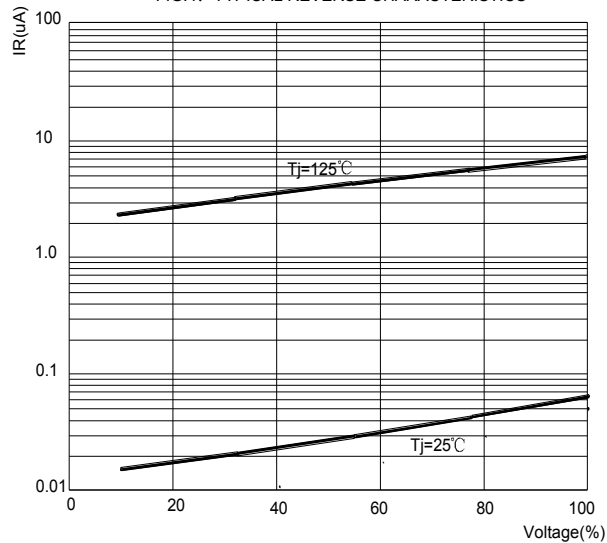
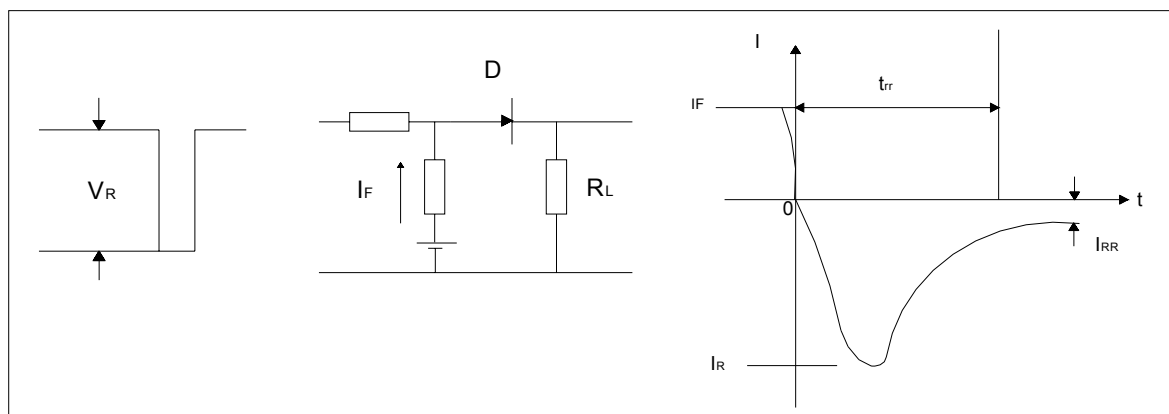
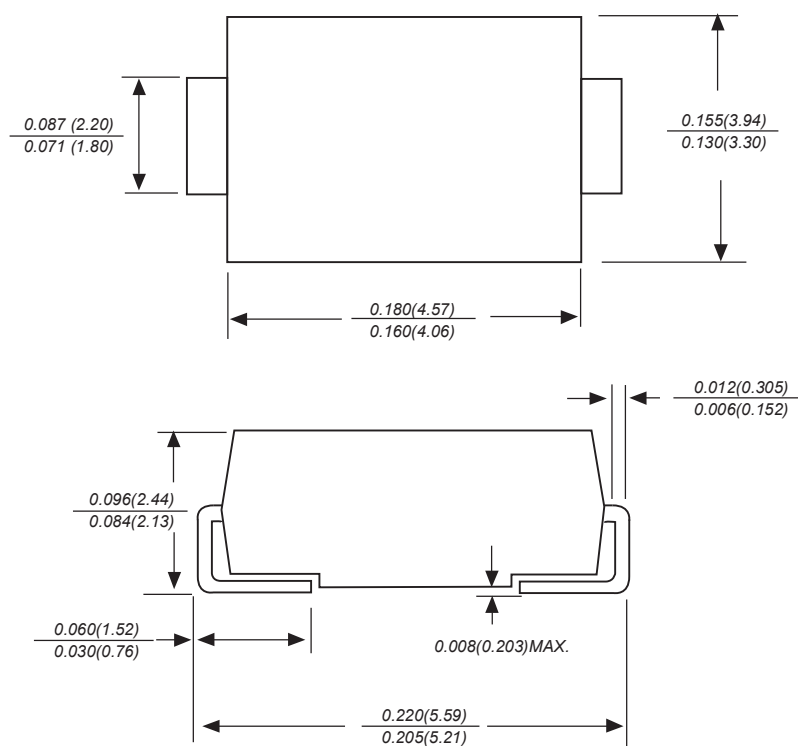


FIG.5: Diagram of circuit and Testing wave form of reverse recovery time

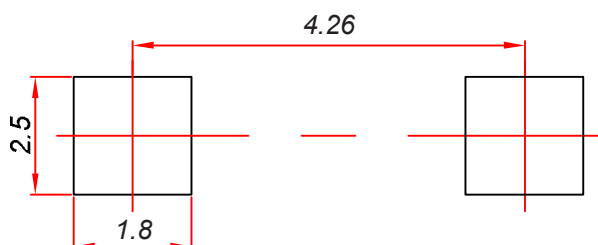


SMBG Package Outline Dimensions



Dimensions in inches and (millimeters)

SMBG Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

Ordering Information

Part Number	Package	Shipping Quantity
ES2A-ES2J	SMBG	3000/tape&Reel

Marking Diagram



X: From A To J

Reel Taping Specifications For Surface Mount Devices–SMBG

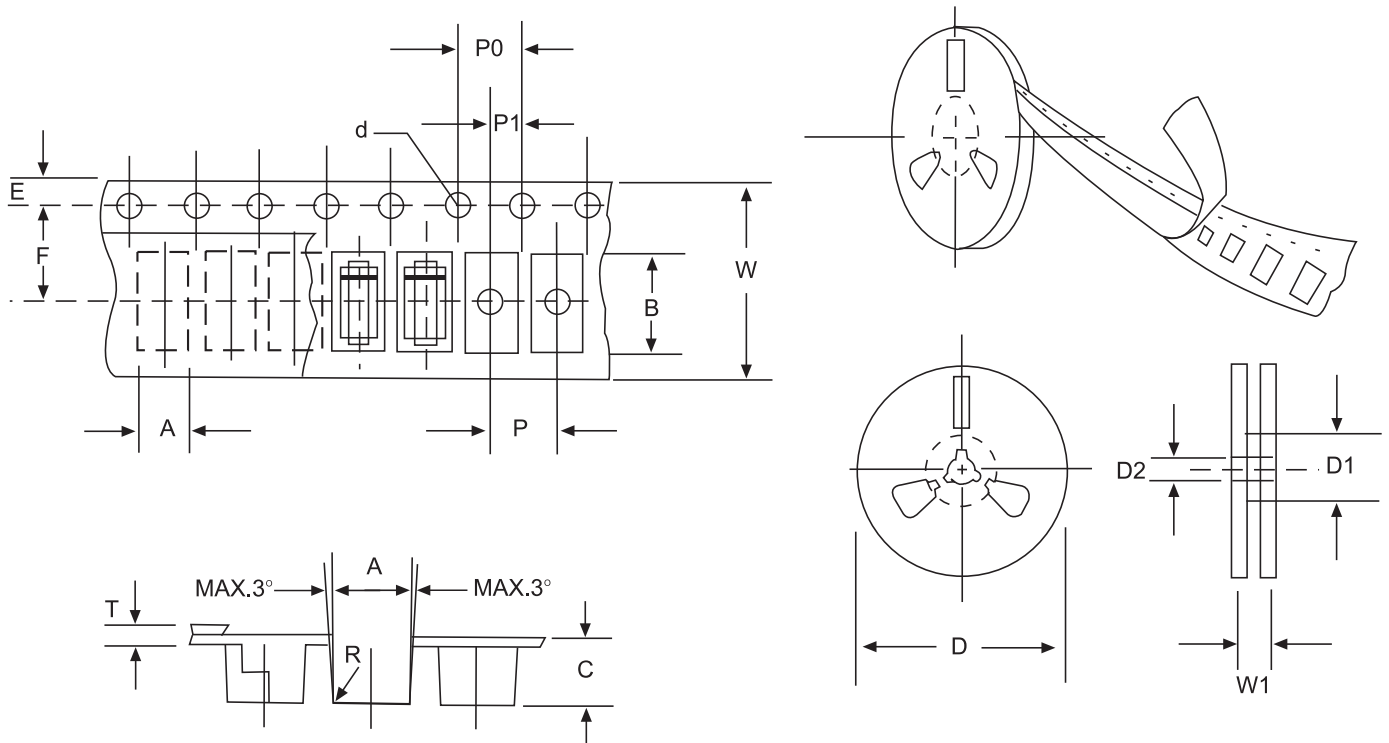


FIG: CONFIGURATION OF SURFACE MOUNTED DEVICES TAPING

ITEM	SYMBOL	SMBG mm(inch)
Carrier width	A	4.09±0.1(0.161±0.004)
Carrier length	B	5.82±0.1(0.229±0.004)
Carrier depth	C	2.50±0.1(0.100±0.004)
Sprocket hole	d	1.55±0.05 (0.061±0.002)
Reel outside diameter	D	330±2.0(13±0.079)
Reel inner diameter	D1	75 ±1.0 (2.95 ±0.039)
Feed hole diameter	D2	13±0.5(0.512±0.020)
Stroket hole position	E	1.75±0.1(0.069±0.004)
Punch hole position	F	5.65±0.05(0.222±0.002)
Punch hole pitch	P	8.0±0.1(0.315±0.004)
Sprocket hole pitch	P0	4.0±0.1(0.157±0.004)
Embossment center	P1	2.0±0.1(0.079±0.004)
Totall tape thickness	T	0.32±0.1 (0.013±0.004)
Tape width	W	12.0±0.2(0.472±0.008)
Reel width	W1	16.8±2.0(0.661±0.079)

NOTE: Devices are packde in accordance with EIA standard RS-481-A and specification given above.